## **PATENT APPLICATION**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Application No.: New U.S. Patent Application

Filed: November 21, 2003 Docket No.: 117849

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

## PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please consider the following:

Amendments to the Specification;

Remarks.